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Counsel for Defendants

FORTRESS INVESTMENT GROUP LLC,
FORTRESS CREDIT CO. LLC,
VLSI TECHNOLOGY LLC

UNITED STATES DISTRICT COURT

NORTHERN DISTRICT OF CALIFORNIA

INTEL CORPORATION and APPLE INC.,

Plaintiffs,

v.

FORTRESS INVESTMENT GROUP LLC,
FORTRESS CREDIT CO. LLC, UNILOC
2017 LLC, UNILOC USA, INC., UNILOC
LUXEMBOURG S.A.R.L., VLSI
TECHNOLOGY LLC, INVT SPE LLC,
INVENTERGY GLOBAL, INC., IXI IP, LLC,
and SEVEN NETWORKS, LLC,

Defendants.

Case No. 3:19-cv-07651-EMC

**DECLARATION OF LUCAS S.
OXENFORD IN SUPPORT OF
DEFENDANTS' JOINT MOTION TO
DISMISS AND STRIKE PLAINTIFFS'
SECOND AMENDED COMPLAINT**

Hon. Edward M. Chen

1 I, Lucas S. Oxenford, declare as follows:

2 1. I am an attorney at law, admitted to practice in the United States District Court,
3 Northern District of California. I am an associate with the law firm of Irell & Manella LLP, and I
4 am counsel for defendants Fortress Investment Group LLC, Fortress Credit Co. LLC, and VLSI
5 Technology LLC in this matter. I have personal knowledge of each fact stated in this declaration
6 and, if called as a witness, I could and would competently and truthfully testify thereto.

7 2. I submit this declaration in support of Defendants' Joint Motion to Dismiss and
8 Strike Plaintiffs' Second Amended Complaint.

9 3. Attached as Exhibit 1 is a true and correct copy of U.S. Patent No. 7,653,508.

10 4. Attached as Exhibit 2 is a true and correct copy of U.S. Patent No. 7,690,556.

11 5. Attached as Exhibit 3 is a true and correct copy of U.S. Patent No. 7,881,902.

12 6. Attached as Exhibit 4 is a true and correct copy of U.S. Patent No. 8,712,723.

13 7. Attached as Exhibit 5 is a true and correct copy of U.S. Patent No. 8,872,646.

14 8. Attached as Exhibit 6 is a true and correct copy of U.S. Patent No. 9,712,986.

15 9. Attached as Exhibit 7 is a true and correct copy of U.S. Patent No. 7,299,008.

16 10. Attached as Exhibit 8 is a true and correct copy of U.S. Patent No. 7,523,331.

17 11. Attached as Exhibit 9 is a true and correct copy of U.S. Patent No. 7,434,009.

18 12. Attached as Exhibit 10 is a true and correct copy of U.S. Patent No. 6,058,437.

19 13. Attached as Exhibit 11 is a true and correct copy of U.S. Patent No. 6,856,616.

20 14. Attached as Exhibit 12 is a true and correct copy of U.S. Patent No. 9,286,466.

21 15. Attached as Exhibit 13 is a true and correct copy of U.S. Patent No. 7,606,983.

22 16. Attached as Exhibit 14 is a true and correct copy of U.S. Patent No. 5,659,687.

23 17. Attached as Exhibit 15 is a true and correct copy of U.S. Patent No. 8,769,296.

24 18. Attached as Exhibit 16 is a true and correct copy of U.S. Patent No. 5,579,222.

25 19. Attached as Exhibit 17 is a true and correct copy of U.S. Patent No. 7,220,220.

26 20. Attached as Exhibit 18 is a true and correct copy of U.S. Patent No. 6,736,759.

27 21. Attached as Exhibit 19 is a true and correct copy of U.S. Patent No. 6,215,403.

28 22. Attached as Exhibit 20 is a true and correct copy of U.S. Patent No. 8,995,433.

23. On April 22, 2021, I performed a search for all patents that have issued from January 1, 2000 until April 22, 2021 containing some variation of the words “cache,” “miss,” and “stall,” within the claims. I used the Patent Full-Text Database (“PaFT”) which is maintained by the United States Patent and Trademark Office (“USPTO”). I accessed the PaFT Database at the following web address: <http://patft.uspto.gov/>. A true and correct screenshot of the first page of this search is attached as Exhibit 21 at 2-3.

24. On April 22, 2021, I performed a search for all patents that have issued from January 1, 2000 until April 22, 2021 containing some variation of the phrase “voice message” and some variation of the word “network” within the claims. I used the PaFT Database. A true and correct screenshot of the first page of this search is attached as Exhibit 21 at 4-5.

25. Attached as Exhibit 22 is a true and correct copy of excerpts of Hance Huston’s September 30, 2020 Rebuttal Expert Report in *VLSI Technology LLC v. Intel Corporation*, 1:18-cv-00966-CFC-CJB (D. Del.).

26. Attached as Exhibit 23 is a true and correct copy of excerpts of the patent purchase agreement between Defendant VLSI Technology LLC and NXP B.V, dated June 30, 2016.

I declare under penalty of perjury under the laws of the United States of America that the foregoing is true and correct to the best of my knowledge.

Executed on April 26, 2021 in Los Angeles, California.

By: /s/ Lucas S. Oxenford
Lucas S. Oxenford